

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	271085	control adj system	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:15
S2	15853	control adj system and passive	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:17
S3	8233	control adj system and passive and performance	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:17
S4	4072	control adj system and passive and performance and load	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:18
S5	546	control adj system and passive and performance and load and oscillation	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:18
S6	75	control adj system and passive and performance and load and oscillation and confidence	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:18
S7	72	control adj system and passive and performance and load and oscillation and confidence and pattern \$1	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:18
S8	69	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:19
S9	62	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:19
S10	62	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display and error	US-PGPUB; USPAT	OR	OFF	2007/11/07 19:52

S11	62	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display and error and feedback	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:19
S12	6	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display and error and feedback and plant	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:21
S13	62	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display and error and feedback and active	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:21
S14	56	control adj system and passive and performance and load and oscillation and confidence and pattern \$1 and repeat\$3 and display and error and feedback and active and discrete	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:24
S15	56104	performance.ab.	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:25
S16	8457	performance.ab. and control.ab.	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:25
S17	37	performance.ab. and control.ab. and passive.ab.	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:27
S18	5785	evaluat\$4 same performance same control	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:28
S19	446	evaluat\$4 same performance same control adj system	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:28
S20	9	evaluat\$4 same performance same control adj system and autocorrelation	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:29

S21	225	load adj disturbance	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:30
S22	0	load adj disturbance adj detectin	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:30
S23	6	load adj disturbance adj detection	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:31
S24	0	oscillation adj detection adj tect	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:32
S25	1	oscillation adj detection adj test	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:32
S26	11	oscillation adj detection same test	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:37
S27	5	oscillation adj detection same test and performance	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:30
S28	1	load adj disturbance and autocorrelation	US-PGPUB; USPAT	OR	OFF	2005/07/24 21:51
S29	615	performance and evaluat\$4 and hvac	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:31
S30	210	performance same evaluat\$4 and hvac	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:42
S31	11629	performance same evaluat\$4 and sequence and changes	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:43
S32	1859	performance same evaluat\$4 and sequence same changes	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:43
S33	4142	performance same evaluat\$4 and sequence same changes active adj test \$3	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:43
S34	3357	performance same evaluat\$4 and sequence same changes active adj test \$3 and data	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:43
S35	1972	performance same evaluat\$4 and sequence same changes active adj test \$3 and data and calculate and parameter	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:44

S36	1960	performance same evaluat\$4 and sequence same changes active adj test \$3 and data and calculate and parameter and input	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:44
S37	1859	performance same evaluat\$4 and sequence same changes active adj test \$3 and data and calculate and parameter and input and control and plant	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:44
S38	1859	performance same evaluat\$4 and sequence same changes active adj test \$3 and data and calculate and parameter and input and control and plant and system	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:45
S39	0	performance same evaluat\$4 and sequence same changes and active adj test\$3 and data and calculate and parameter and input and control and plant and system	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:45
S40	0	performance same evaluat\$4 and sequence same changes and active adj test\$3 and data and calculate and parameter	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:45
S41	10	performance same evaluat\$4 and sequence same changes and active adj test\$3 and data	US-PGPUB; USPAT	OR	OFF	2005/07/24 22:45
S42	1	"4769817".pn.	US-PGPUB; USPAT	OR	OFF	2005/09/19 16:22
S43	2801	active and passive and hysteresis	US-PGPUB; USPAT	OR	OFF	2005/09/19 16:34

S44	748	active and passive and hysteresis and control adj system	US-PGPUB; USPAT	OR	OFF	2005/09/19 16:35
S45	387	active and passive and hysteresis and control adj system and processor	US-PGPUB; USPAT	OR	OFF	2005/09/19 16:35
S46	195	active and passive and hysteresis and control adj system and processor and testing	US-PGPUB; USPAT	OR	OFF	2005/09/19 16:35
S47	190	active and passive and hysteresis and control adj system and processor and testing and data	US-PGPUB; USPAT	OR	OFF	2005/09/19 17:10
S48	1015	702/182.ccls.	US-PGPUB; USPAT	OR	OFF	2005/09/19 17:10
S49	50	700/26.ccls.	US-PGPUB; USPAT	OR	OFF	2005/09/19 17:10
S50	2178	performance and monitor\$3 and power adj controller	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:38
S51	1395	performance and monitor\$3 and power adj controller and communication	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:40
S52	676	performance and monitor\$3 and power adj controller and communication and module	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:40
S53	459	performance and monitor\$3 and power adj controller and communication and module and error	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:40
S54	209	performance and monitor\$3 and power adj controller and communication and module and error and consumption	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:40
S55	187	performance and monitor\$3 and power adj controller and communication and module and error and consumption and speed	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:41

S56	187	performance and monitor\$3 and power adj controller and communication and module and error and consumption and speed and process\$4	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:41
S57	156	performance and monitor\$3 and power adj controller and communication and module and error and consumption and speed and process\$4 and electrical	US-PGPUB; USPAT	OR	OFF	2006/08/07 10:41
S58	84	performance and monitor\$3 and power adj controller and communication and module and error and consumption and speed and process\$4 and electrical and characteristic	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:00
S59	114	performance and monitor\$3 and power adj controller and communication and error and consumption and speed and process \$4 and electrical and characteristic	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:02
S60	126	performance and monitor\$3 and power adj controller and communication and (error or fault) and consumption and speed and process\$4 and electrical and characteristic	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:02
S61	42	performance and monitor\$3 and power adj controller and communication and (error or fault) and consumption and speed and process\$4 and electrical and characteristic not S58	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:13

S62	11	monitor\$3 same performance and power adj controller and communication and (error or fault) and consumption and speed and process\$4 and electrical and characteristic not \$58	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:14
S63	13	monitor\$3 same performance and power adj controller and communication and (error or fault) and consumption and process\$4 and electrical and characteristic not \$58	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:15
S64	60	monitor\$3 same performance and power adj controller and communication and (error or fault) and process\$4 and electrical and characteristic	US-PGPUB; USPAT	OR	OFF	2006/08/07 11:17
S65	1456	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic	US-PGPUB; USPAT	OR	ON	2006/08/07 11:18
S66	200	monitor\$3 same performance and power adj controller and communication and (error or fault) and process\$4 and characteristic	US-PGPUB; USPAT	OR	ON	2006/08/07 11:22
S67	176	monitor\$3 same performance and power adj controller and communication and (error or fault) and process\$4 and characteristic and circuit	US-PGPUB; USPAT	OR	ON	2006/08/07 11:22

S68	1272	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit	US-PGPUB; USPAT	OR	ON	2006/08/07 11:22
S69	993	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply	US-PGPUB; USPAT	OR	ON	2006/08/07 11:23
S70	551	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority	US-PGPUB; USPAT	OR	ON	2006/08/07 11:25
S71	496	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate	US-PGPUB; USPAT	OR	ON	2006/08/07 11:25
S72	442	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed	US-PGPUB; USPAT	OR	ON	2006/08/07 11:25

S73	439	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed and change	US-PGPUB; USPAT	OR	ON	2006/08/07 11:25
S74	428	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed and change and compar\$3	US-PGPUB; USPAT	OR	ON	2006/08/07 11:26
S75	433	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed and change and compar\$4	US-PGPUB; USPAT	OR	ON	2006/08/07 11:26
S76	345	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed and change and compar\$4 and temperature	US-PGPUB; USPAT	OR	ON	2006/08/07 11:26

S77	204	monitor\$3 same performance and power adj control\$3 and communication and (error or fault) and process\$4 and characteristic and circuit and supply and priority and rate and speed and change and compar\$4 and temperature and electrical	US-PGPUB; USPAT	OR	ON	2007/03/09 15:03
S78	35	second adj sub-module and monitor\$3 and performance	US-PGPUB; USPAT	OR	ON	2007/03/09 15:10
S79	11317	on-board and monitor \$3 and performance	US-PGPUB; USPAT	OR	ON	2007/03/09 15:10
S80	0	on adj board and monitor\$3 and performance	US-PGPUB; USPAT	OR	ON	2007/03/09 15:10
S81	5122	on-board and monitor \$3 and performance and chip	US-PGPUB; USPAT	OR	ON	2007/03/09 15:11
S82	4533	on-board and monitor \$3 and performance and chip and circuit	US-PGPUB; USPAT	OR	ON	2007/03/09 15:11
S83	492173	on-board and monitor \$3 and performance and chip and circuit module	US-PGPUB; USPAT	OR	ON	2007/03/09 15:11
S84	2852	on-board and monitor \$3 and performance and chip and circuit and module	US-PGPUB; USPAT	OR	ON	2007/03/09 15:11
S85	2164	on-board and monitor \$3 and performance and chip and circuit and module and third	US-PGPUB; USPAT	OR	ON	2007/03/09 15:11
S86	2048	on-board and monitor \$3 and performance and chip and circuit and module and third and power	US-PGPUB; USPAT	OR	ON	2007/03/09 15:12
S87	2040	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control	US-PGPUB; USPAT	OR	ON	2007/03/09 15:12

S88	1870	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated	US-PGPUB; USPAT	OR	ON	2007/03/09 15:12
S89	1870	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3	US-PGPUB; USPAT	OR	ON	2007/03/09 15:13
S90	1723	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external	US-PGPUB; USPAT	OR	ON	2007/03/09 15:13
S91	1638	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5	US-PGPUB; USPAT	OR	ON	2007/03/09 15:13
S92	1027	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor	US-PGPUB; USPAT	OR	ON	2007/03/09 15:14
S93	902	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption)	US-PGPUB; USPAT	OR	ON	2007/03/09 15:14

S94	804	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$4	US-PGPUB; USPAT	OR	ON	2007/03/09 15:15
S95	805	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5	US-PGPUB; USPAT	OR	ON	2007/03/09 15:15
S96	698	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error	US-PGPUB; USPAT	OR	ON	2007/03/09 15:16
S97	249	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error adj rate	US-PGPUB; USPAT	OR	ON	2007/03/09 15:16

S98	249	on-board and monitor \$3 and performance and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error adj rate and time	US-PGPUB; USPAT	OR	ON	2007/03/09 16:08
S99	1	on-board and monitor \$3 and performance adj sensor and chip and circuit and module and third and power and control and integrated and monitor\$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error adj rate and time	US-PGPUB; USPAT	OR	ON	2007/03/09 16:09
S100	11317	on-board and monitor \$3 and performance nearsensor and chip and circuit and module and third and power and control and integrated and monitor \$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error adj rate and time	US-PGPUB; USPAT	OR	ON	2007/03/09 16:09

S101	6	on-board and monitor \$3 and performance near sensor and chip and circuit and module and third and power and control and integrated and monitor \$3 and external and communicat\$5 and sensor and (temperature or consumption) and adjust\$5 and error adj rate and time	US-PGPUB; USPAT	OR	ON	2007/03/09 16:09
S102	0	"2006052962"	US-PGPUB; USPAT	OR	ON	2007/03/12 16:23
S103	2	"20060052962"	US-PGPUB; USPAT	OR	ON	2007/03/12 16:23
S104	7043	program\$6 adj logic and noise and monitor \$4	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:37
S105	4	("6823293", "6788098", "6628135", "5550840"). pn.	US-PGPUB; USPAT	OR	OFF	2007/03/18 14:22
S106	2596	program\$6 adj logic and noise and monitor \$4 and buffer	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:42
S107	1763	program\$6 adj logic and noise and monitor \$4 and buffer and measur\$4	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:46
S108	1832	program\$6 adj logic and noise and monitor \$4 and buffer and measur\$5	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:46
S109	402972	program\$6 adj logic and noise and monitor \$4 and buffer and measur\$5 power adj supply	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:47
S110	27668	program\$6 adj logic and monitor\$4 and buffer and measur\$5 power adj supply same noise	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:47
S111	193	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:47

S112	193	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise and output	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:47
S113	193	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise and output and input	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:48
S114	186	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:48
S115	181	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and voltage	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:48
S116	103	program\$6 adj logic and monitor\$4 and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:50
S117	0	program\$6 adj logic and monitor\$4 and differential adj buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:50
S118	1	program\$6 adj logic and monitor\$4 and differential same buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:51

S119	86	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:56
S120	76	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:57
S121	74	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:57
S122	74	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:57
S123	74	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:59

S124	74	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin	US-PGPUB; USPAT	OR	OFF	2007/03/18 13:59
S125	77	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin	US-PGPUB; USPAT	OR	ON	2007/03/18 14:11
S126	77	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range	US-PGPUB; USPAT	OR	ON	2007/03/18 14:12
S127	77	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4	US-PGPUB; USPAT	OR	ON	2007/03/18 14:12

S128	0	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and dat	US-PGPUB; USPAT	OR	ON	2007/03/18 14:12
S129	77	program\$6 adj logic and monitor\$4 and differential and buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 14:33
S130	0	program\$6 adj logic and monitor\$4 and differential adj input adj buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 14:34
S131	66	program\$6 adj logic and monitor\$4 and input adj buffer and measur\$5 and power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 14:43

S132	57	program\$6 adj logic and monitor\$4 and input adj buffer and measur\$5 same power adj supply same noise and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:13
S133	1	plc and measur\$4 adj power adj supply adj noise	US-PGPUB; USPAT	OR	ON	2007/03/18 14:49
S134	4	plc and measur\$4 adj signal adj noise	US-PGPUB; USPAT	OR	ON	2007/03/18 14:49
S135	1	plc and measur\$4 adj signal adj noise and buffer	US-PGPUB; USPAT	OR	ON	2007/03/18 14:50
S136	24	programmable adj logic and measur\$4 adj noise and buffer	US-PGPUB; USPAT	OR	ON	2007/03/18 14:53
S137	13	programmable adj logic and measur\$4 adj noise and buffer and supply	US-PGPUB; USPAT	OR	ON	2007/03/18 14:54
S138	11	programmable adj logic and measur\$4 adj noise and buffer and supply and compar\$6	US-PGPUB; USPAT	OR	ON	2007/03/18 14:55
S139	11	programmable adj logic and measur\$4 adj noise and buffer and supply and compar\$6 and reference	US-PGPUB; USPAT	OR	ON	2007/03/18 14:55
S140	9	programmable adj logic and measur\$4 adj noise and buffer and supply and compar\$6 and reference same voltage	US-PGPUB; USPAT	OR	ON	2007/03/18 15:08
S141	21	programmable adj logic and measur\$4 adj noise and supply and compar\$6 and reference same voltage	US-PGPUB; USPAT	OR	ON	2007/03/18 15:11

S142	9	programmable adj logic and monitor\$3 adj noise and supply and compar\$6 and reference same voltage	US-PGPUB; USPAT	OR	ON	2007/03/18 15:12
S143	0	program\$6 adj logic and monitor\$4 and input adj buffer and measur\$5 and power adj supply and signal-to-noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:15
S144	0	program\$6 adj logic and measur\$4 and input adj buffer and measur\$5 and power adj supply and signal-to-noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:15
S145	0	program\$6 adj logic and input adj buffer and measur\$5 and power adj supply and signal-to-noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:15

S146	0	program\$6 adj logic and input adj buffer and measur\$5 and power adj supply and signal adj noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and low and pin and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:16
S147	0	program\$6 adj logic and input adj buffer and measur\$5 and power adj supply and signal adj noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and range and var\$4 and data	US-PGPUB; USPAT	OR	ON	2007/03/18 15:16
S148	0	program\$6 adj logic and input adj buffer and measur\$5 and power adj supply and signal adj noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and range	US-PGPUB; USPAT	OR	ON	2007/03/18 15:17
S149	33	program\$6 adj logic and input and buffer and measur\$5 and power adj supply and signal adj noise adj ratio and output and input and compar\$5 and reference adj voltage and inverter and register and clock and range	US-PGPUB; USPAT	OR	ON	2007/03/18 15:18
S150	24992	measur\$5 and noise. ab.	US-PGPUB; USPAT	OR	ON	2007/03/19 09:54
S151	1	"20070046325"	US-PGPUB; USPAT	OR	ON	2007/11/07 22:50

S152	1153853	program\$5	US-PGPUB; USPAT	OR	OFF	2007/11/07 19:53
S153	1471123	program\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 19:53
S154	347401	program\$5 and logic	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 19:53
S155	9151	program\$5 and logic and input-output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 19:54
S156	15	program\$5 and logic and input-output and differential adj input adj buffer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 19:56
S157	5	program\$5 and logic and input-output and differential adj input adj buffer and noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 20:17
S158	0	program\$5 and logic and input-output and differential adj input adj buffer and noise and monitor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 20:19
S159	0	program\$5 and logic and input-output and differential adj input adj buffer and noise and monitor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 20:19
S160	5	program\$5 and logic and input-output and differential adj input adj buffer and noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 20:22
S161	3	program\$5 and logic and input-output and differential adj input adj buffer and noise and power	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:19

S162	156	differential adj input adj buffer and power adj supply	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:20
S163	58	differential adj input adj buffer and power adj supply adj voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:20
S164	2	differential adj input adj buffer and power adj supply adj voltage same compare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:24
S165	0	differential adj input adj buffer and power adj supply adj noise same compare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:24
S166	0	differential adj input adj buffer and power adj supply adj noise and compare	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:24
S167	5	differential adj input adj buffer and power adj supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:27
S168	5	differential adj input adj buffer and power same supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:27
S169	4512	differential adj input adj buffer and powerand supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:27
S170	7	differential adj input adj buffer and power and supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:28
S171	1485	buffer and power adj supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:28

S172	816	differential and buffer and power adj supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:28
S173	1842957	differential and buffer and power adj supply adj noise input and output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:28
S174	791	differential and buffer and power adj supply adj noise and input and output	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:28
S175	171	differential and buffer and power adj supply adj noise and input and output and monitoring	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:29
S176	1	differential and buffer and power adj supply adj noise and input and output and monitoring adj circuitry	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:31
S177	0	comparator adj noise adj monitoring adj circuitry	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:32
S178	4	noise adj monitoring adj circuitry	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:33
S179	367	input adj buffer and power adj supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:34
S180	111	input adj buffer and power adj supply adj noise and monitor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:34
S181	100	input adj buffer and power adj supply adj noise and monitor\$3 and compar\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:35

S182	99	input adj buffer and power adj supply adj noise and monitor\$3 and compar\$4 and voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:35
S183	50	input adj buffer and power adj supply adj noise and monitor\$3 and compar\$4 and voltage and power adj supply adj voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:36
S184	48	input adj buffer and power adj supply adj noise and monitor\$3 and compar\$4 and voltage and power adj supply adj voltage and logic	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:36
S185	22	input adj buffer and power adj supply adj noise and monitor\$3 and compar\$4 and voltage and power adj supply adj voltage and logic and programmable	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:45
S186	18176	voltage and reference and programmable adj logic	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:46
S187	1281	voltage adj reference and programmable adj logic	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:46
S188	1	voltage adj reference and programmable adj logic and noise adj monitor\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:47
S189	6	buffer and programmable adj logic and noise adj monitor \$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:50
S190	0	post-comparator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:50

S191	3	post-comparator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:52
S192	153	noise adj conditions and power adj supply adj voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 21:53
S193	12	noise adj conditions same power adj supply adj voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 22:01
S194	0	buffer adj compares adj voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 22:02
S195	16	measur\$3 adj power adj supply adj noise	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 22:03
S196	1	measur\$3 adj power adj supply adj noise and differential	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	ON	2007/11/07 22:31
S197	8201	i/o adj circuit	US-PGPUB; USPAT	OR	ON	2007/11/07 22:50
S198	11642	i/o adj circuit\$3	US-PGPUB; USPAT	OR	ON	2007/11/07 22:50
S199	0	i/o adj circuit\$3 and measur\$4 adj power adj supply adj noise	US-PGPUB; USPAT	OR	ON	2007/11/07 22:51
S200	0	i/o adj circuit\$3 and measur\$4 adj power adj supply and noise	US-PGPUB; USPAT	OR	ON	2007/11/07 22:51
S201	7	i/o adj circuit\$3 and measur\$4 adj power adj supply and noise	US-PGPUB; USPAT	OR	ON	2007/11/07 22:52
S202	0	i/o adj circuit\$3 and differential adj power adj supply and noise	US-PGPUB; USPAT	OR	ON	2007/11/07 22:52
S203	739	i/o adj circuit\$3 and differential and power adj supply and noise	US-PGPUB; USPAT	OR	ON	2007/11/07 22:52

S204	12	i/o adj circuit\$3 and differential adj input adj buffer and power adj supply and noise	US-PGPUB; USPAT	OR	ON	2007/11/07 23:57
S205	16	i/o adj circuit\$3 and differential adj input adj buffer and power adj supply and voltage	US-PGPUB; USPAT	OR	ON	2007/11/07 23:57
S206	732	signal and variable adj amplifier and noise	USPAT	OR	OFF	2008/02/02 16:13
S207	320	signal and variable adj amplifier and noise and feedback	USPAT	OR	OFF	2008/02/02 16:14
S208	69	signal and variable adj amplifier and noise and feedback and feed	USPAT	OR	OFF	2008/02/02 16:14
S209	31	signal and variable adj amplifier and noise and feedback and feed and proportional	USPAT	OR	OFF	2008/02/02 16:17
S210	11	signal and variable adj amplifier and noise and feedback and feed adj forward and proportional	USPAT	OR	OFF	2008/02/02 16:20
S211	5	signal and variable adj amplifier and noise and feedback and feed adj forward and proportional and controller	USPAT	OR	OFF	2008/02/02 16:27
S212	2	signal and variable adj amplifier and noise and feedback and feed adj forward same amplifier and proportional and controller	USPAT	OR	OFF	2008/02/02 16:28

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